To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1526, 2SK1527

Silicon N Channel MOS FET

REJ03G0950-0300
Rev.3.00
May 13, 2009

Application

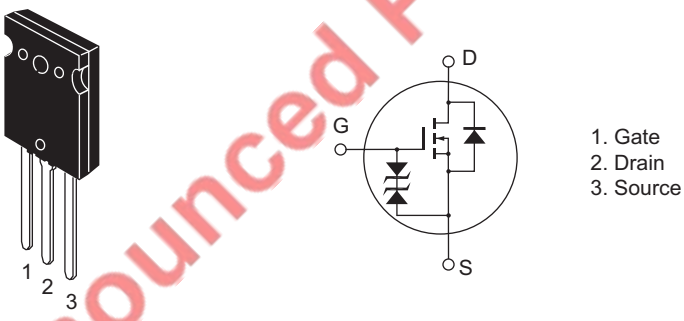
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

RENESAS Package code: PRSS0004ZF-A
(Package name: TO-3PL)



Absolute Maximum Ratings

(Ta = 25°C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1526	V_{DSS}	450	V
	2SK1527		500	
Gate to source voltage		V_{GSS}	±30	V
Drain current		I_D	40	A
Drain peak current		$I_{D(pulse)}$*[1]	160	A
Body to drain diode reverse drain current		I_{DR}	40	A
Channel dissipation		Pch*[2]	250	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at T_C = 25°C

Electrical Characteristics

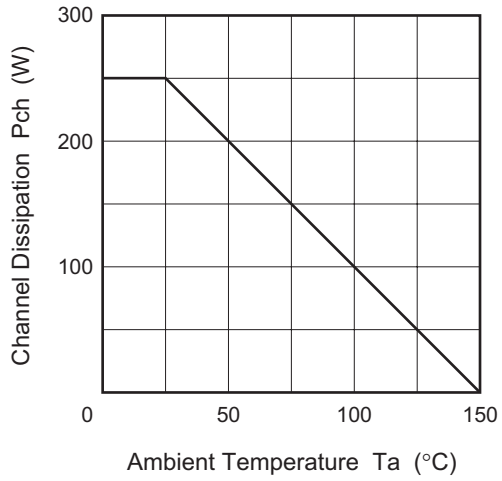
(Ta = 25°C)

Item		Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK1526	$V_{(BR)DSS}$	450	—	—	V	I_D = 10 mA, V_{GS} = 0
	2SK1527		500				
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 µA, V_{DS} = 0
Gate to source leak current		I_{GSS}	—	—	±10	µA	V_{GS} = ±25 V, V_{DS} = 0
Zero gate voltage drain current	2SK1526	I_{DSS}	—	—	250	µA	V_{DS} = 360 V, V_{GS} = 0
	2SK1527						V_{DS} = 400 V, V_{GS} = 0
Gate to source cutoff voltage		$V_{GS(off)}$	2.0	—	3.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	2SK1526	$R_{DS(on)}$	—	0.11	0.15	Ω	I_D = 20 A, V_{GS} = 10 V *[3]
	2SK1527		—	0.12	0.16		
Forward transfer admittance		$\|y_{fs}\|$	20	30	—	S	I_D = 20 A, V_{DS} = 10 V *[3]
Input capacitance		Ciss	—	5800	—	pF	V_{DS} = 10 V, V_{GS} = 0, f = 1 MHz
Output capacitance		Coss	—	1430	—	pF	
Reverse transfer capacitance		Crss	—	150	—	pF	
Turn-on delay time		$t_{d(on)}$	—	60	—	ns	I_D = 20 A, V_{GS} = 10 V, R_L = 1.5 Ω
Rise time		t_r	—	175	—	ns	
Turn-off delay time		$t_{d(off)}$	—	420	—	ns	
Fall time		t_f	—	160	—	ns	
Body to drain diode forward voltage		V_{DF}	—	1.2	—	V	I_F = 40 A, V_{GS} = 0
Body to drain diode reverse recovery time		t_{rr}	—	600	—	ns	I_F = 40 A, V_{GS} = 0, di_F/dt = 100 A/µs

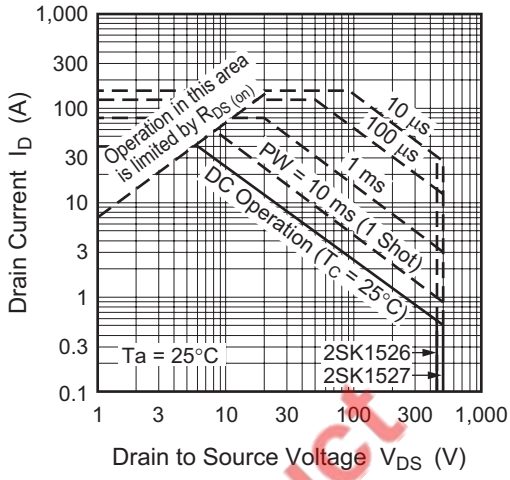
Note: 3. Pulse test

Main Characteristics

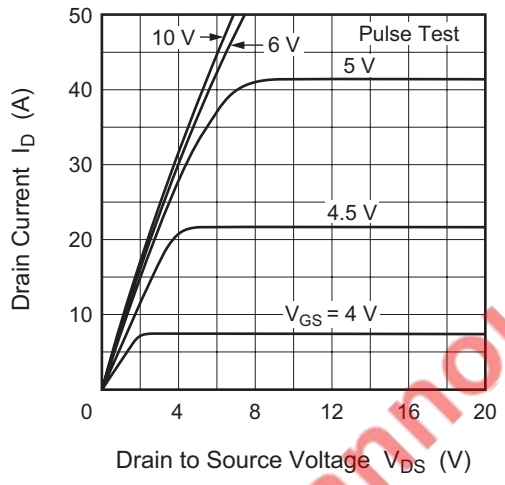
Power vs. Temperature Derating



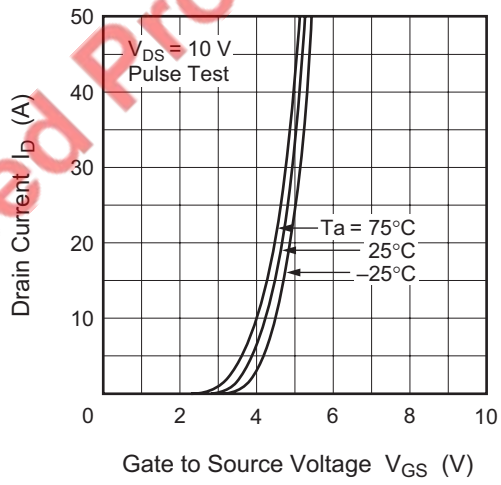
Maximum Safe Operation Area



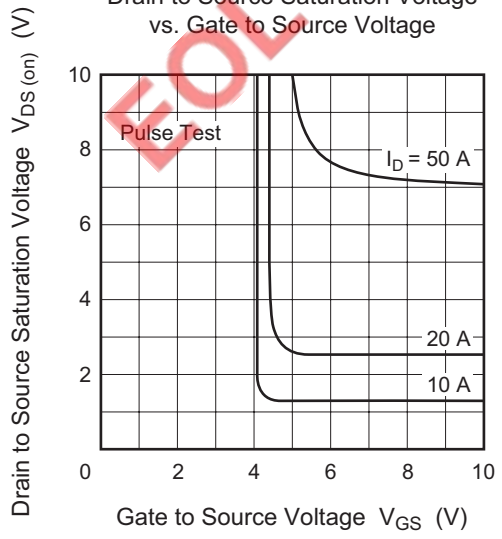
Typical Output Characteristics



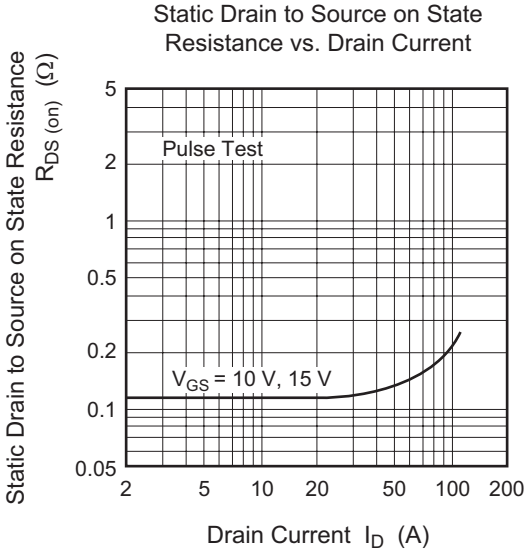
Typical Transfer Characteristics



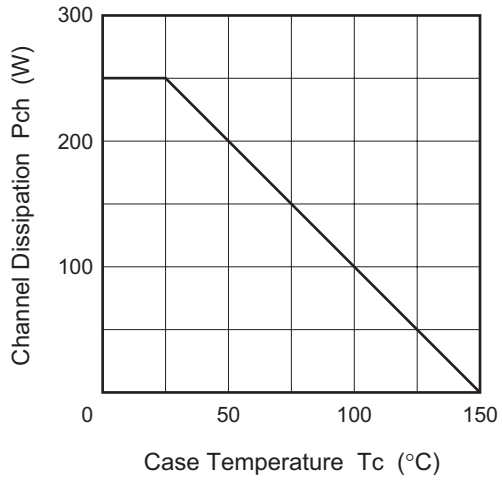
Drain to Source Saturation Voltage vs. Gate to Source Voltage



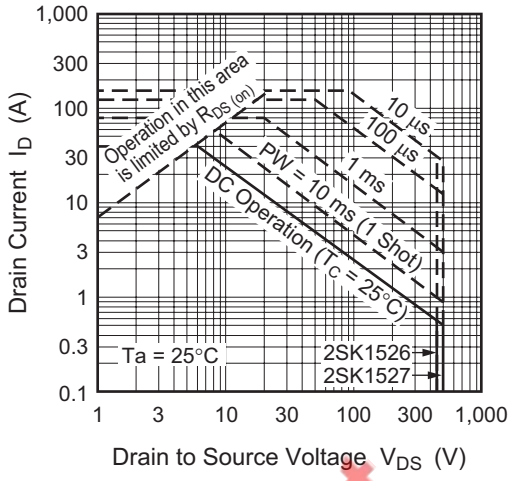
Static Drain to Source on State Resistance vs. Drain Current



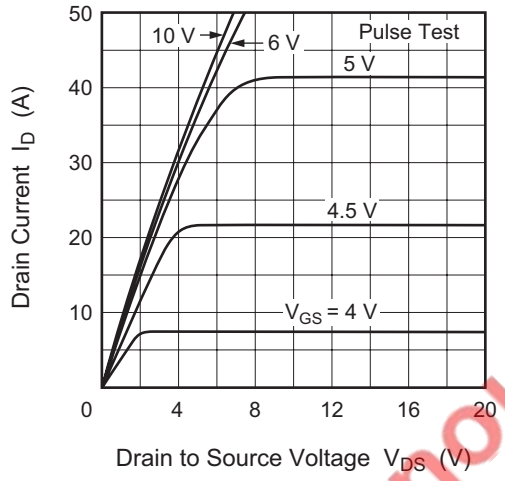
Power vs. Temperature Derating



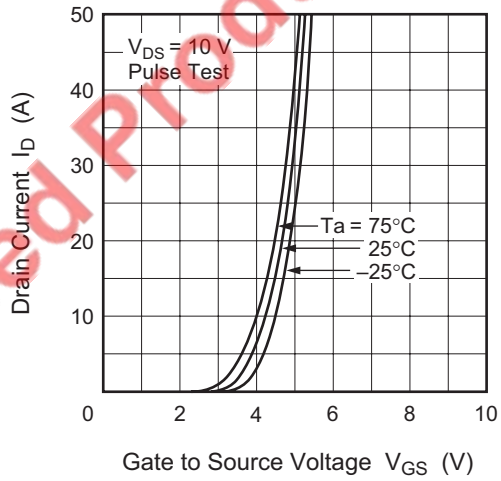
Maximum Safe Operation Area



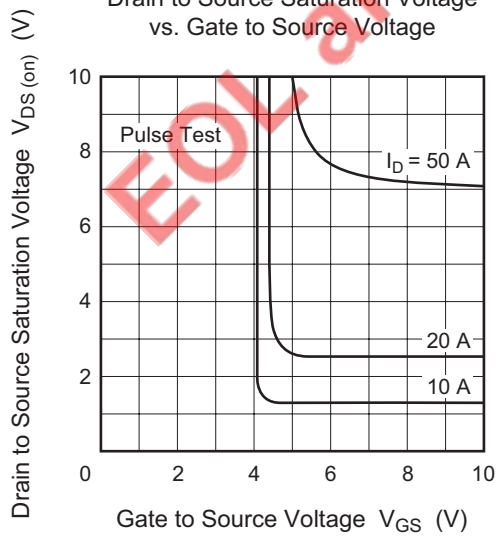
Typical Output Characteristics



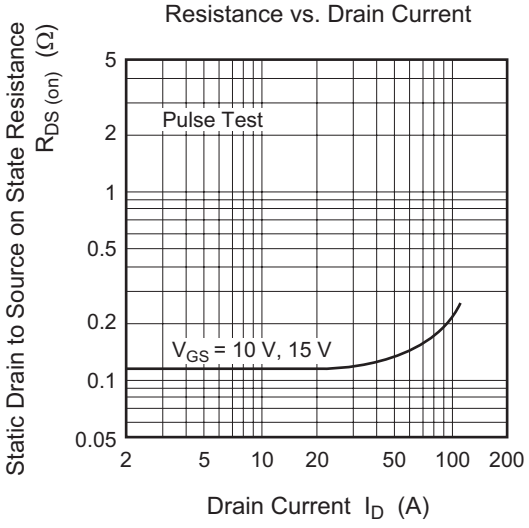
Typical Transfer Characteristics



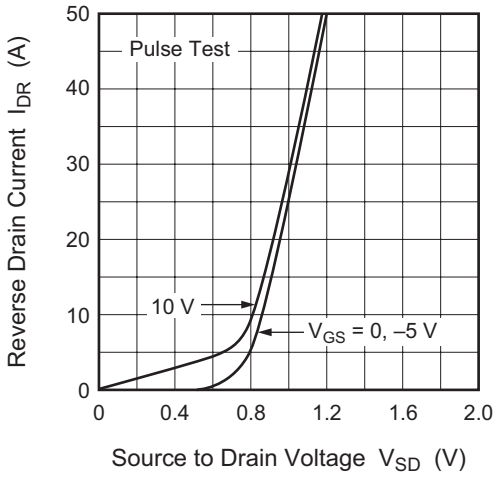
Drain to Source Saturation Voltage vs. Gate to Source Voltage



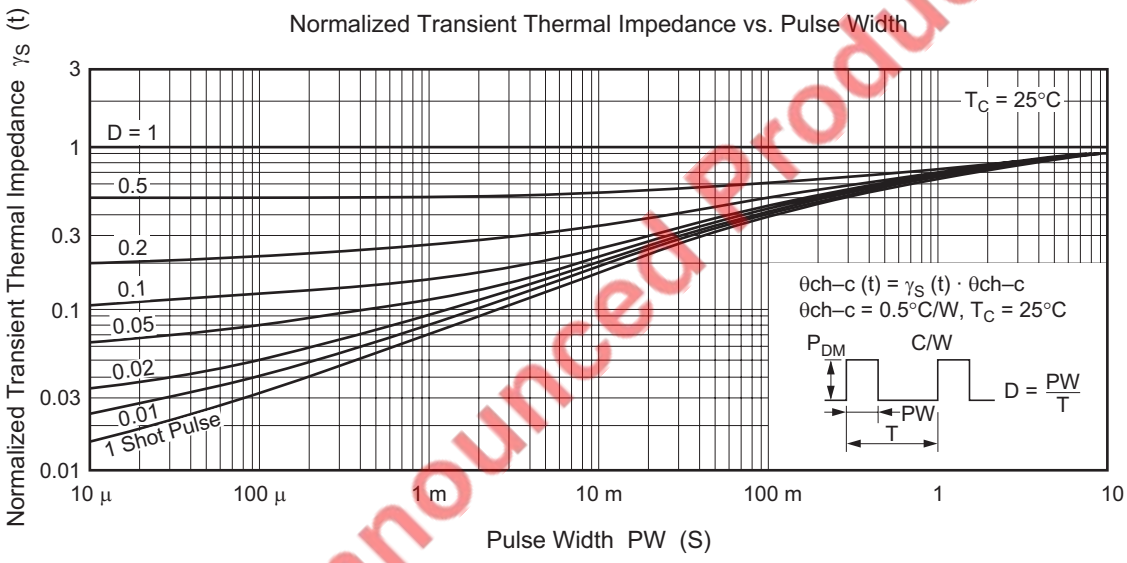
Static Drain to Source on State Resistance vs. Drain Current



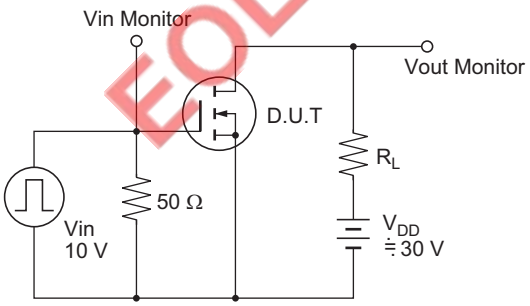
Reverse Drain Current vs.
Source to Drain Voltage



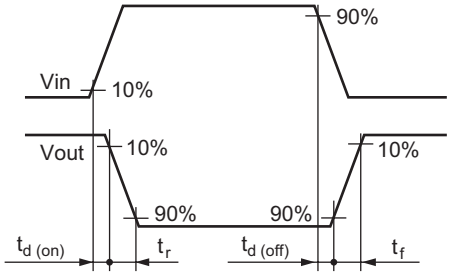
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



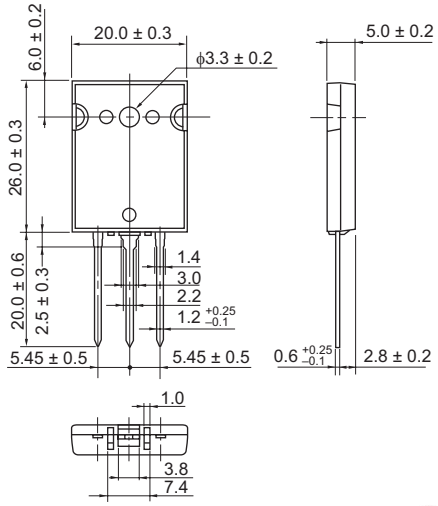
Waveforms



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-3PL	—	PRSS0004ZF-A	TO-3PL / TO-3PLV	9.9g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SK1526-E	250 pcs	Box (Tube)
2SK1527-E	250 pcs	Box (Tube)

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Colophon .7.2