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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR 2SK1591

PACKAGE DRAWING (Unit: mm)

2.8 ±0.2

1.5

0.4 +0.1

95

C

0.3

2.9 ±0.2

to 1.4

N-CHANNEL MOSFET FOR SWITCHING

DESCRIPTION

The 2SK1591, N-channel vertical type MOSFET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOSFET has excellent switching characteristics and high drain to source voltage, it is suitable for applications requiring high voltage and high-speed.

FEATURES

- Directly driven by ICs having a 5 V power source.
- Not necessary to consider driving current because of its high input impedance.
- Has high voltage and high-speed switching characteristics.

ORDERING INFORMATION

PART NUMBER	PACKAGE	C
2SK1591	SC-59 (Mini Mold)	

Marking: G18

ABSOLUTE MAXIMUM RATINGS (TA= 25°C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	100	V
Gate to Source Voltage (Vos = 0 V)	Vgss	±20	V
Drain Current (DC)	D(DC)	±200	mA
Drain Current (pulse)	D(pulse)	±400	mA
Total Power Dissipation	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

1 Marking

> - 9 - 9

> > 0.16

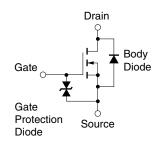
0.1

0 to 1. Source

2. Gate

3. Drain

0.65 +0.1 -0.15



EQUIVALENT CIRCUIT

Note PW \leq 10 ms, Duty Cycle \leq 50%

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

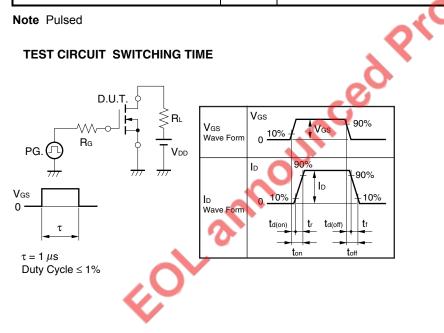
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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

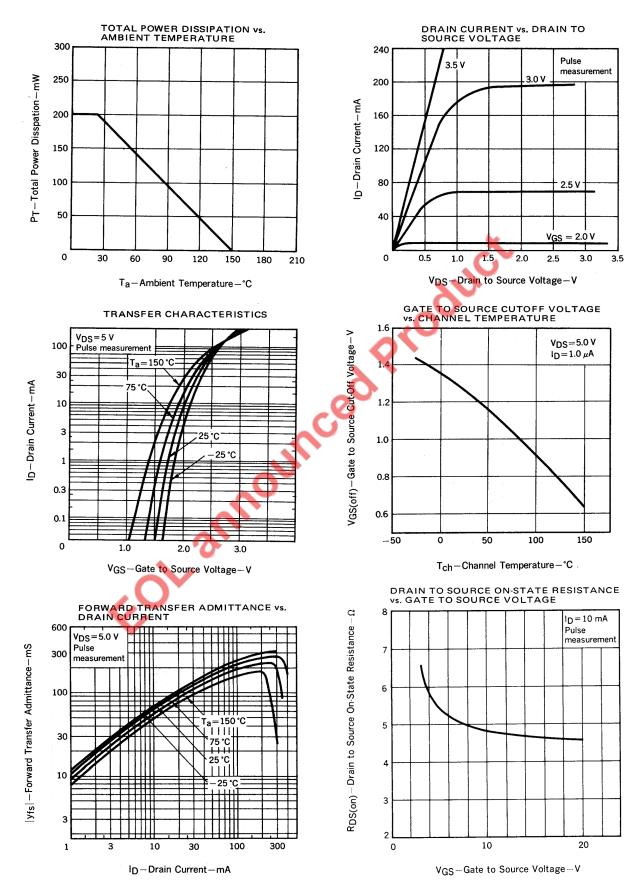
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V			1.0	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±1.0	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 5.0 V, I _D = 1.0 μA	0.8	1.3	1.8	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 5.0 V, I _D = 10 mA	20	60		mS
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 4.0 V, I _D = 10 mA		5.8	8.0	Ω
	RDS(on)2	V _{GS} = 10 V, I _D = 10 mA		4.8	6.5	Ω
Input Capacitance	Ciss	V _{DS} = 5.0 V		25		pF
Output Capacitance	Coss	V _{GS} = 0 V		15		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		2.0		pF
Turn-on Delay Time	td(on)	V _{DD} = 5.0 V, I _D = 10 mA		60		ns
Rise Time	tr	V _{GS} = 5.0 V		100		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		180		ns
Fall Time	tr)	140		ns

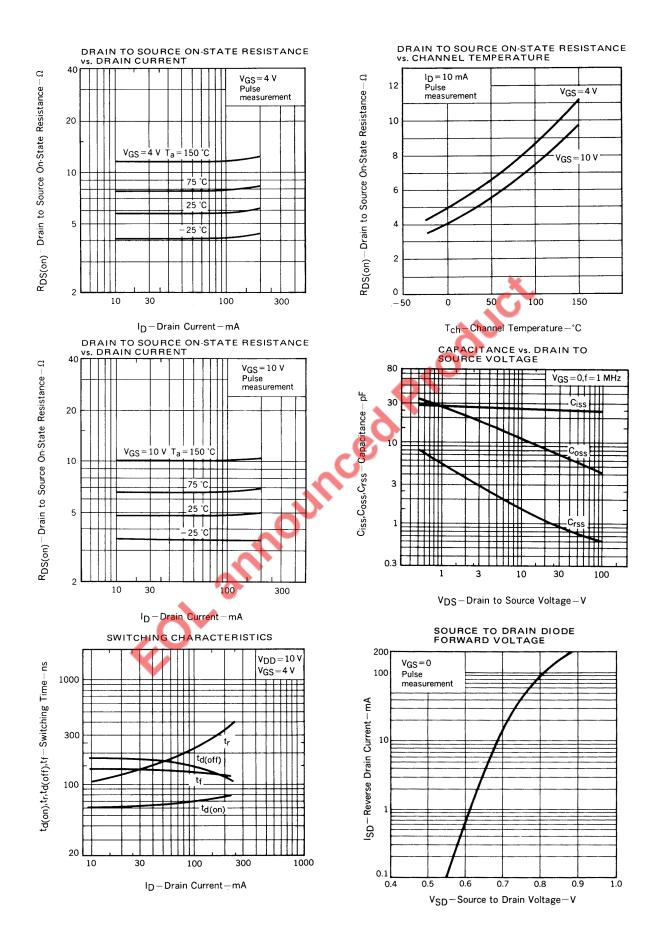
Note Pulsed

TEST CIRCUIT SWITCHING TIME



TYPICAL CHARACTERISTICS (TA = 25^{\circ}C)





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