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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1772 Silicon N Channel MOS FET

REJ03G0971-0200 (Previous: ADE-208-1318) Rev.2.00 Sep 07, 2005

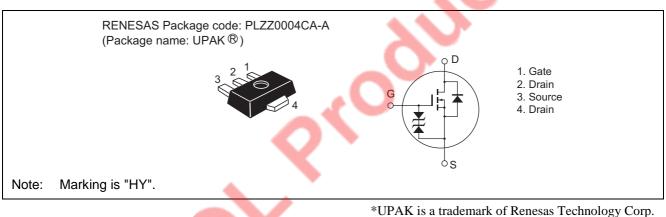
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source.
- Suitable for DC-DC converter, motor drive, power switch, solenoid drive







Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	1	А
Drain peak current	I _{D(pulse)} *1	2	А
Body to drain diode reverse drain current	I _{DR}	1	А
Channel dissipation	Pch ^{*2}	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 $\mu s,$ duty cycle \leq 1 %

2. When using the alumina ceramic board (12.5 \times 20 \times 0.7mm)

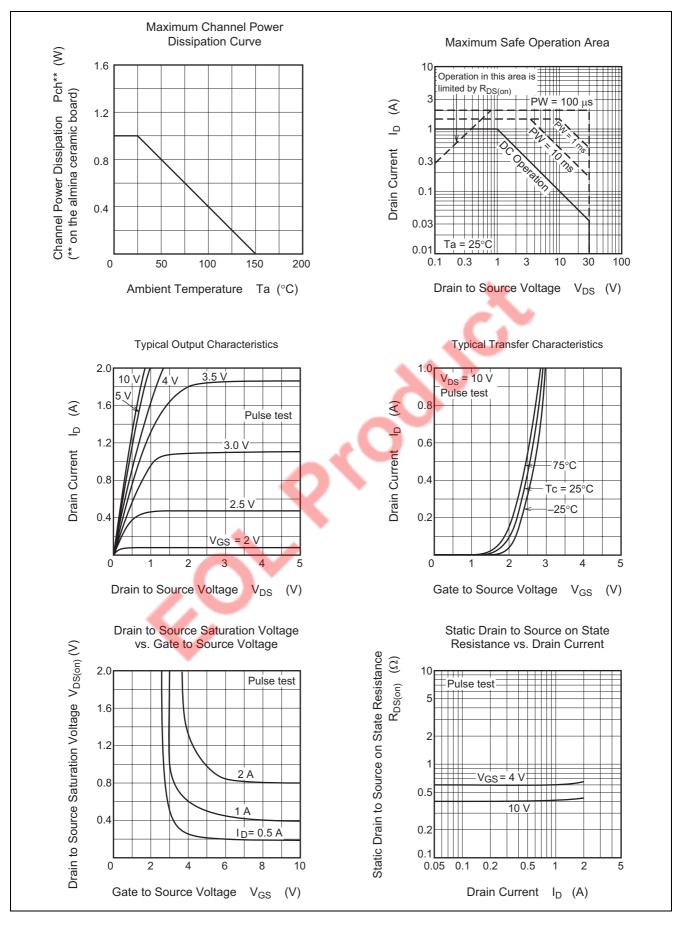
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	_	—	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	$I_{G} = \pm 100 \ \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	50	μA	$V_{DS} = 25 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.0	_	2.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state	R _{DS(on)}	_	0.4	0.6	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
resistance		_	0.6	0.85	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{*3}$
Forward transfer admittance	y _{fs}	0.6	1.0) -	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	Ciss		85	_	pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0,$ f = 1 MHz
Output capacitance	Coss		65	—	pF	
Reverse transfer capacitance	Crss		20	—	pF	
Turn-on delay time	t _{d(on)}		10	_	ns	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V},$ $R_L = 60 \ \Omega^{*3}$
Rise time	↓ t _r	_	15	_	ns	
Turn-off delay time	t _{d(off)}		40	_	ns	
Fall time	tf	_	30	_	ns	
Body to drain diode forward voltage	V _{DF}	_	1.2	—	V	$I_F = 1 \text{ A}, V_{GS} = 0^{*3}$
Body to drain diode reverse	t _{rr}	_	30	_	ns	$I_F = 1 \text{ A}, V_{GS} = 0,$
recovery time						$di_F/dt = 50 A/\mu s^{*3}$
Note: 3 Pulse Test						

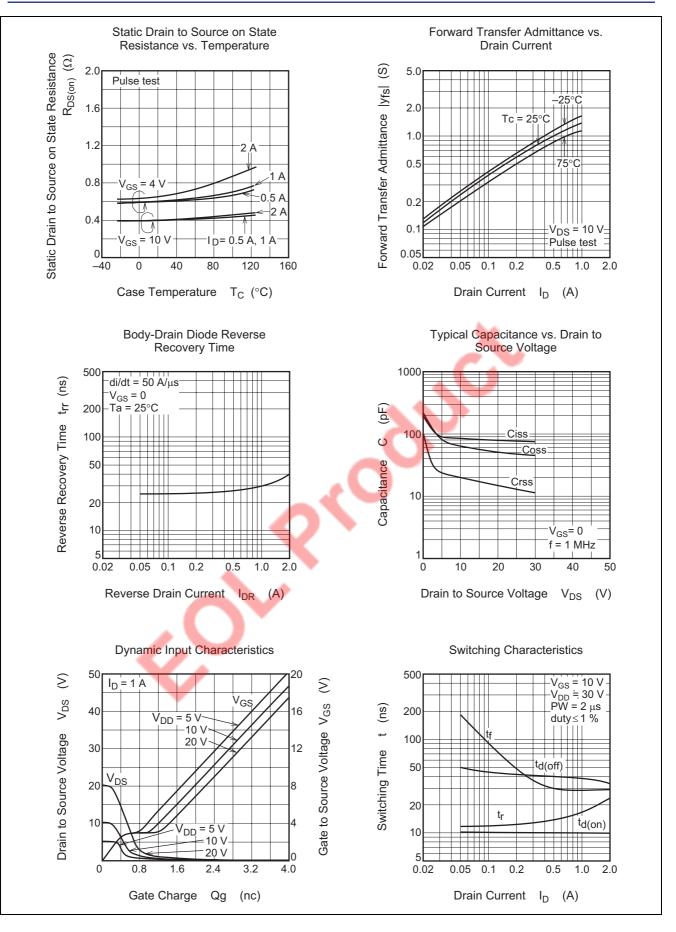
Note: 3. Pulse Test



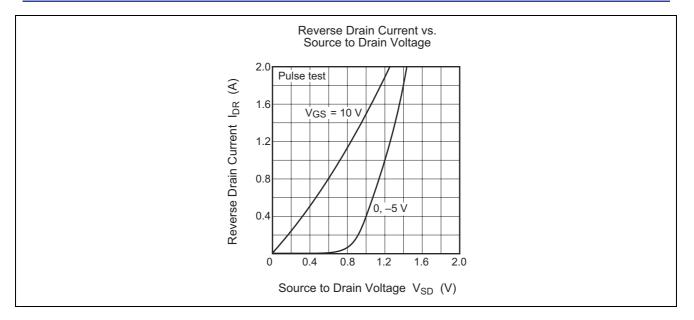
Main Characteristics







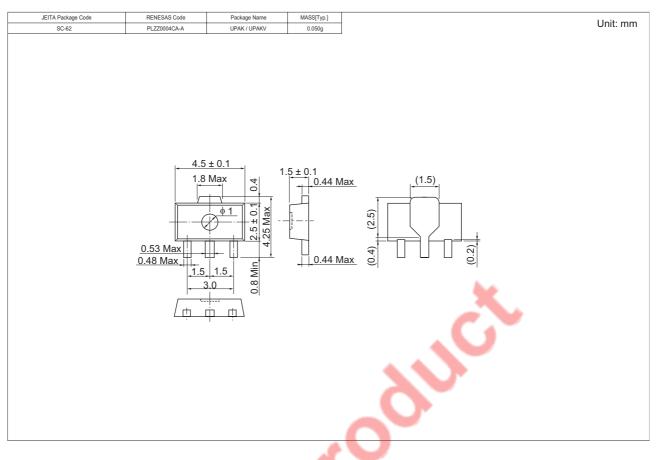




Loui voltage V_{SD} (V)



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK1772HYTR-E	3000 pcs 🔪 📃	Taping, φ178 mm Reel

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