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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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## MOS FIELD EFFECT TRANSISTOR 2SK1657

### **N-CHANNEL MOSFET** FOR SWITCHING

#### DESCRIPTION

The 2SK1657 is an N-channel vertical type MOSFET which can be driven by 2.5 V power supply.

As the MOSFET is low Gate Leakage Current, it is suitable for filter circuit.

### **FEATURES**

- Directly driven by ICs having a 3 V power supply.
- · Has low Gate Leakage Current  $lgss = \pm 5 \text{ nA MAX.} (Vgs = \pm 3.0 \text{ V})$

### ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK1657	SC-59 (Mini Mold)

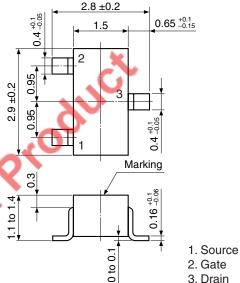
Marking: G19

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	Voss	30	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±7.0	V
Drain Current (DC)	ID(DC)	±100	mΑ
Drain Current (pulse) Note	I <sub>D(pulse)</sub>	±200	mA
Total Power Dissipation	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

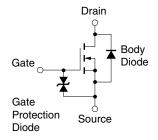
**Note** PW  $\leq$  10 ms, Duty Cycle  $\leq$  50%

### PACKAGE DRAWING (Unit: mm)



3. Drain

### **EQUIVALENT CIRCUIT**



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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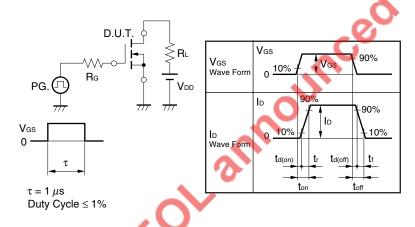
Document No. D17806EJ2V0DS00 (2nd edition) (Previous No. TC-2360) Date Published November 2005 NS CP(K)

**ELECTRICAL CHARACTERISTICS (TA = 25°C)** 

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	V <sub>GS</sub> = ±3.0 V, V <sub>DS</sub> = 0 V			±5.0	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 1.0 μA	0.9	1.2	1.5	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 10 mA	20	40		mS
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 10 mA		25	45	Ω
	RDS(on)2	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 10 mA		18	25	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 3.0 V		15		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		10		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1.5		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 3.0 V, I <sub>D</sub> = 10 mA		50		ns
Rise Time	tr	V <sub>GS</sub> = 3 V	. (	23		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		34		ns
Fall Time	tr		<b>3</b>	43		ns

Note Pulsed

### **TEST CIRCUIT SWITCHING TIME**

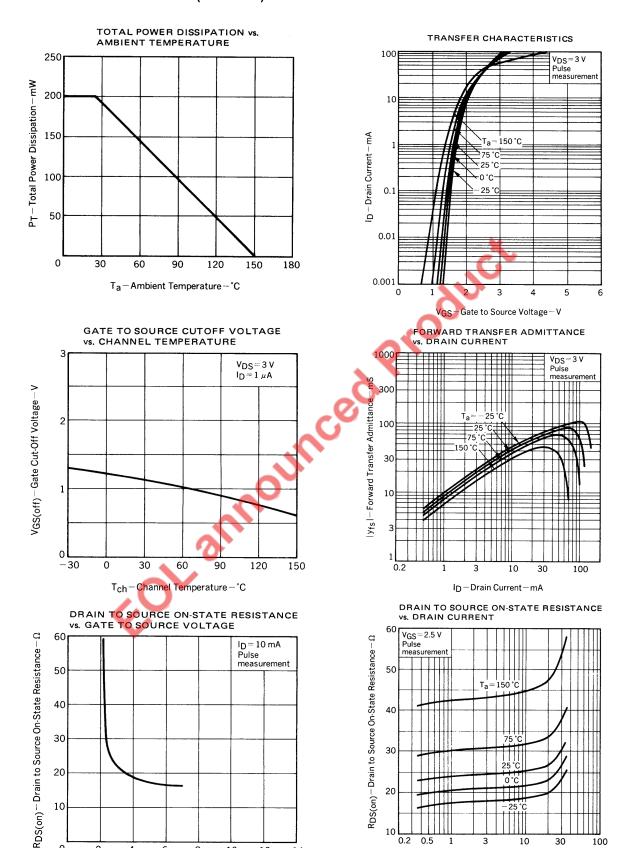


0

2

VGS - Gate to Source Voltage - V

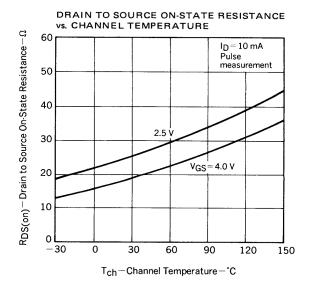
### TYPICAL CHARACTERISTICS (TA = 25°C)

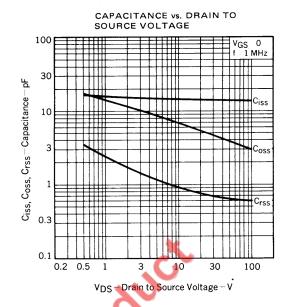


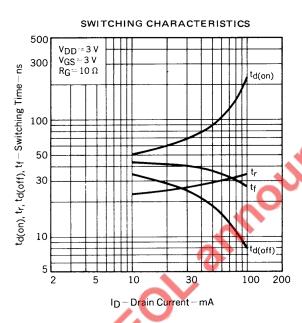
3

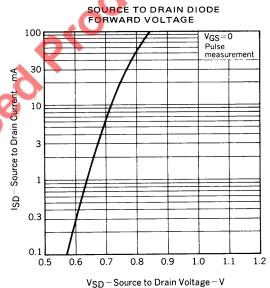
ID - Drain Current - mA

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