

Middle Power LED Series
2835 1.0W 9V Hot bin Ra80

LM283N+

RM Rank

NITE



Designed for better lm/\$ (Lamps)

Features & Benefits

- 0.9W Class mid power LED
- Standard form factor for design flexibility (2.8 × 3.5mm)
- Human-centric lighting



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1. Characteristics

a) Absolute Maximum Rating

Item	Symbol	Rating	Unit	Condition
Ambient / Operating Temperature	T_a	-40 ~ +85	°C	-
Storage Temperature	T_{stg}	-40 ~ +85	°C	-
LED Junction Temperature	T_j	125	°C	-
Forward Current	I_F	110	mA	-
Peak Pulsed Forward Current	I_{FP}	200	mA	Duty 1/10, pulse width 10ms
Assembly Process Temperature	-	260 <10	°C s	-
ESD (HBM)	-	2	kV	-

Note:

Proper current derating must be observed to maintain junction temperature below the maximum at all time.

b) Electro-optical Characteristics ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)

Item	Unit	Rank	Bin	Min.	Typ.	Max.
Forward Voltage (VF)	V	SG or SK	GZ	8.8	-	9.1
			G1	9.1	-	9.4
			G2	9.4	-	9.7
			G3	9.7	-	10.0
Color Rendering Index (Ra)	-	5		80	-	-
Special CRI (R9)		-		0		
Melanopic ratio	-	2200K			0.28	
		2700K			0.37	
Thermal Resistance (junction to solder point)	$^\circ\text{C/W}$			-	15	-
Beam Angle	$^\circ$			-	120	-

Note:

Samsung maintains measurement tolerance of: forward voltage = $\pm 0.2 \text{ V}$, CRI = ± 3 , R9 = ± 6.5

Melanopic ratio is provided just for reference only as a design guide.

b) Electro-optical Characteristics ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)

Item	CRI (R_a) Min.	Nominal CCT (K)	Bin	100mA	
				Min.	Max.
Luminous Flux (Φ_v)	80	2200K	RM	85	100
		2700K	RM	90	105

Note:

Samsung maintains measurement tolerance of: forward voltage = $\pm 0.2\text{V}$, luminous flux = $\pm 5\%$, CRI = ± 3

2. Product Code Information

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
S	P	M	W	H	H	N	2	9	A	Q	5	S	G	D	0	R	M

Digit	PKG Information	Code	Specification										
1 2 3	Samsung Package Middle Power	SPM	Middle power										
4 5	Color	WH	White										
6	Product Version	H	Human-centric Light										
7 8 9	Form Factor	N29	2.8 x 3.5 x 0.7 mm; 2 pads; 3chip;										
10	Sorting Current (mA)	A	100 mA										
11	Chromaticity Coordinates	Q	Hot bin										
12	CRI	5	Min. 80										
13 14	Forward Voltage (V)	SG or SK	<table border="1"> <tr> <td rowspan="4">8.8~10.0</td><td rowspan="4">Bin Code</td><td>GZ</td><td>8.8 ~ 9.1</td></tr> <tr> <td>G1</td><td>9.1 ~ 9.4</td></tr> <tr> <td>G2</td><td>9.4 ~ 9.7</td></tr> <tr> <td>G3</td><td>9.7 ~ 10.0</td></tr> </table> <p>SG : 4,000ea per reel ,SK : 16,000ea per reel</p>	8.8~10.0	Bin Code	GZ	8.8 ~ 9.1	G1	9.1 ~ 9.4	G2	9.4 ~ 9.7	G3	9.7 ~ 10.0
8.8~10.0	Bin Code	GZ	8.8 ~ 9.1										
		G1	9.1 ~ 9.4										
		G2	9.4 ~ 9.7										
		G3	9.7 ~ 10.0										
15 16	CCT (K)	D☆ W☆	<table border="1"> <tr> <td>2200</td><td>Bin Code:</td><td>D1, D2, D3, D4, D5, D6, D7, D8, D9, DA, DB, DC, DD, DE, DF, DG</td></tr> <tr> <td>2700</td><td></td><td>W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG</td></tr> </table> <p>☆ : "0" (Whole bin) "M" (Quarter bin) or "K" (kitting bin)</p>	2200	Bin Code:	D1, D2, D3, D4, D5, D6, D7, D8, D9, DA, DB, DC, DD, DE, DF, DG	2700		W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG				
2200	Bin Code:	D1, D2, D3, D4, D5, D6, D7, D8, D9, DA, DB, DC, DD, DE, DF, DG											
2700		W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG											
17 18	Luminous Flux	RM	<table border="1"> <tr> <td>Bin Code:</td><td>RM</td></tr> </table>	Bin Code:	RM								
Bin Code:	RM												

a) Luminous Flux Bins ($I_F = 100 \text{ mA}$, $T_a = 25^\circ\text{C}$)

CRI (R_a) Min.	Nominal CCT (K)	Product Code	Flux Bin	Flux Range (Φ_v , lm)
80	2200	SPMWHHN29AQ5SGD☆RM	RM	85 ~ 100
	2700	SPMWHHN29AQ5SGW☆RM	RM	90 ~ 105

Note: "☆" can be "0" (Whole bin), "M" (Quarter bin) or "K" (Kitting bin) of the color binning

b) Kitting rule

1) Kitting bin Concept

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, Im).
2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (GZ+GZ), (G1+G1), (G2+G2) or (G3+G3).
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)

[Kitting example]

D	E	F	G
9	A	B	C
5	6	7	8
1	2	3	4

[Binning Information]

	Bin #1	Bin #2
VF	GZ	GZ
	G1	G1
	G2	G2
	G3	G3
CIE	1, 2, 5 bin	C, F, G bin
	6, 7, A, B bin	6, 7, A, B bin
	3, 4, 8 bin	9, D, E bin

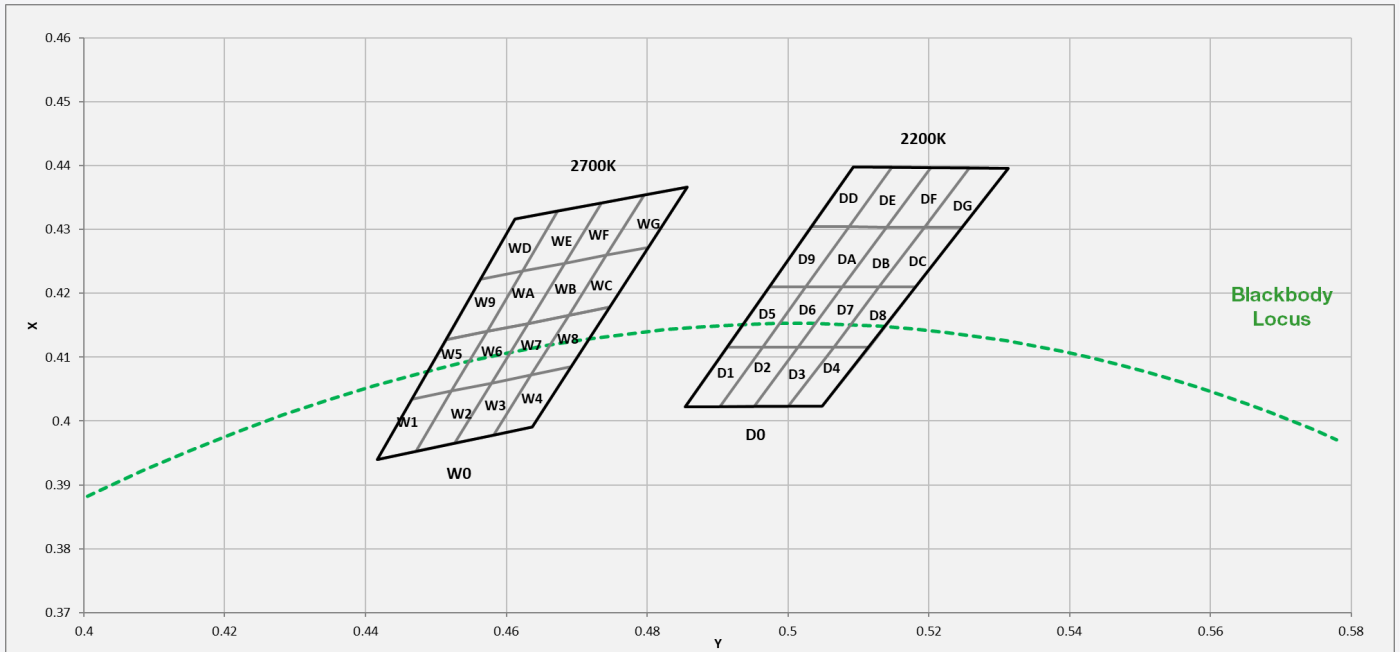
c) Color Bins ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)

CRI (Ra) Min.	Nominal CCT (K)	Product Code	Color Rank	Chromaticity Bins
80	2200	SPMWHHN29AQ5SGD0RM	D0 (Whole bin)	D1, D2, D3, D4, D5, D6, D7, D8, D9, DA, DB, DC, DD, DE, DF, DG
		SPMWHHN29AQ5SGDMRM	DM (Quarter bin)	D6, D7, DA, DB
		SPMWHHN29AQ5SGDKRM	DK (Kitting bin)	D1, D2, D3, D4, D5, D6, D7, D8, D9, DA, DB, DC, DD, DE, DF, DG
	2700	SPMWHHN29AQ5SGW0RM	W0 (Whole bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG
		SPMWHHN29AQ5SGWMRM	WM (Quarter bin)	W6, W7, WA, WB
		SPMWHHN29AQ5SGWKRM	WK (Kitting bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG

d) Voltage Bins ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)

CRI (Ra) Min.	Nominal CCT (K)	Product Code	Voltage Rank	Voltage Bin	Voltage Range (V)
-	-	-		GZ	8.8 ~ 9.1
			SG	G1	9.1 ~ 9.4
			or		
			SK	G2	9.4 ~ 9.7
				G3	9.7 ~ 10.0

e) Chromaticity Region & Coordinates ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)



e) Chromaticity Region & Coordinates

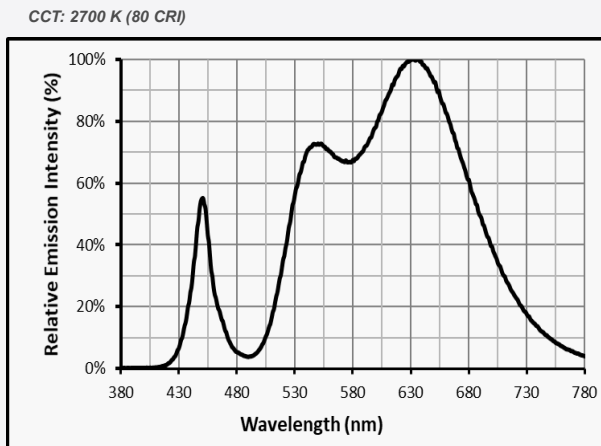
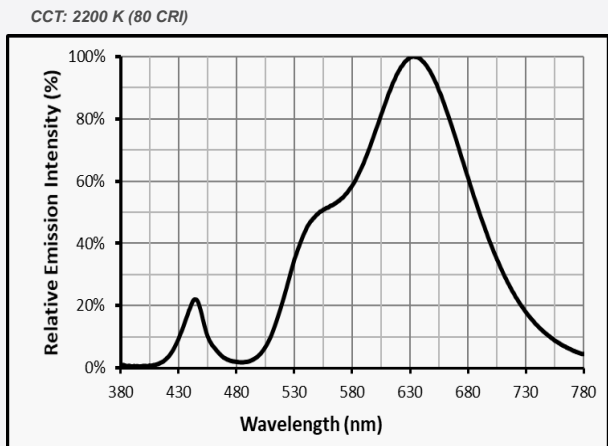
Region	CIE x	CIE y	Region	CIE x	CIE y
D rank (2200 K)					
D1	0.4854	0.4022	D9	0.4974	0.421
	0.4914	0.4116		0.5033	0.4304
	0.4964	0.4116		0.5087	0.4304
	0.4903	0.4022		0.5025	0.421
D2	0.4903	0.4022	DA	0.5025	0.421
	0.4964	0.4116		0.5087	0.4304
	0.5014	0.4116		0.514	0.4303
	0.4952	0.4023		0.5077	0.421
D3	0.4952	0.4023	DB	0.5077	0.421
	0.5014	0.4116		0.514	0.4303
	0.5065	0.4116		0.5194	0.4303
	0.5	0.4023		0.5129	0.421
D4	0.5	0.4023	DC	0.5129	0.421
	0.5065	0.4116		0.5194	0.4303
	0.5115	0.4116		0.5247	0.4303
	0.5049	0.4023		0.5181	0.421
D5	0.4914	0.4116	DD	0.5033	0.4304
	0.4974	0.421		0.5093	0.4398
	0.5025	0.421		0.5148	0.4398
	0.4964	0.4116		0.5087	0.4304
D6	0.4964	0.4116	DE	0.5087	0.4304
	0.5025	0.421		0.5148	0.4398
	0.5077	0.421		0.5203	0.4397
	0.5014	0.4116		0.514	0.4303
D7	0.5014	0.4116	DF	0.514	0.4303
	0.5077	0.421		0.5203	0.4397
	0.5129	0.421		0.5258	0.4397
	0.5065	0.4116		0.5194	0.4303
D8	0.5065	0.4116	DG	0.5194	0.4303
	0.5129	0.421		0.5258	0.4397
	0.5181	0.421		0.5313	0.4396
	0.5115	0.4116		0.5247	0.4303

Region	CIE x	CIE y	Region	CIE x	CIE y
W rank (2700 K)					
W1	0.4417	0.3940	W9	0.4515	0.4128
	0.4472	0.3953		0.4573	0.4141
	0.4522	0.4047		0.4623	0.4235
	0.4466	0.4034		0.4563	0.4222
W2	0.4472	0.3953	WA	0.4573	0.4141
	0.4527	0.3966		0.4631	0.4153
	0.4579	0.4059		0.4683	0.4247
	0.4522	0.4047		0.4623	0.4235
W3	0.4527	0.3966	WB	0.4631	0.4153
	0.4582	0.3978		0.4689	0.4166
	0.4635	0.4072		0.4742	0.4260
	0.4579	0.4059		0.4683	0.4247
W4	0.4582	0.3978	WC	0.4689	0.4166
	0.4637	0.3991		0.4747	0.4179
	0.4692	0.4085		0.4802	0.4272
	0.4635	0.4072		0.4742	0.4260
W5	0.4466	0.4034	WD	0.4563	0.4222
	0.4522	0.4047		0.4623	0.4235
	0.4573	0.4141		0.4673	0.4329
	0.4515	0.4128		0.4612	0.4316
W6	0.4522	0.4047	WE	0.4623	0.4235
	0.4579	0.4059		0.4683	0.4247
	0.4631	0.4153		0.4735	0.4341
	0.4573	0.4141		0.4673	0.4329
W7	0.4579	0.4059	WF	0.4683	0.4247
	0.4635	0.4072		0.4742	0.4260
	0.4689	0.4166		0.4796	0.4354
	0.4631	0.4153		0.4735	0.4341
W8	0.4635	0.4072	WG	0.4742	0.4260
	0.4692	0.4085		0.4802	0.4272
	0.4747	0.4179		0.4857	0.4366
	0.4689	0.4166		0.4796	0.4354

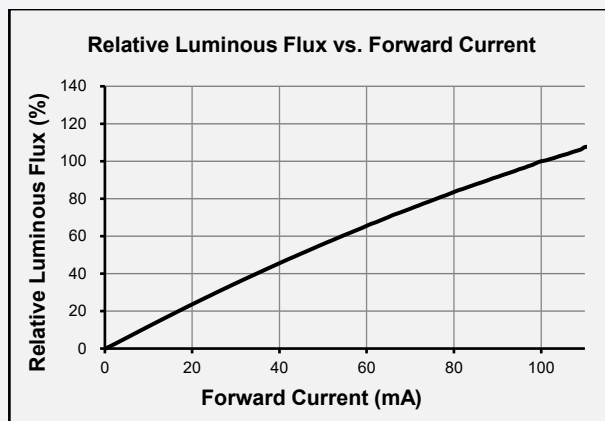
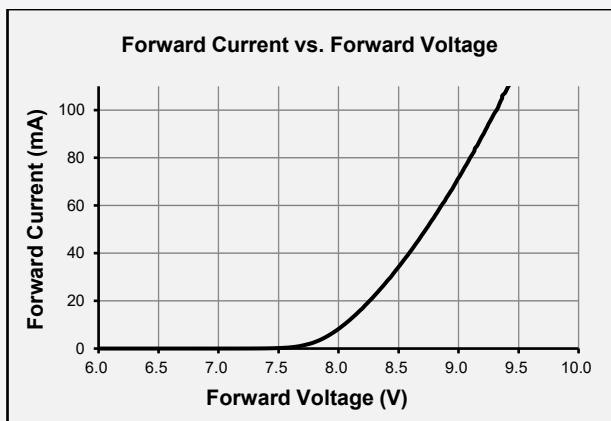
Note: Samsung maintains measurement tolerance of: Cx, Cy = ± 0.005

3. Typical Characteristics Graphs

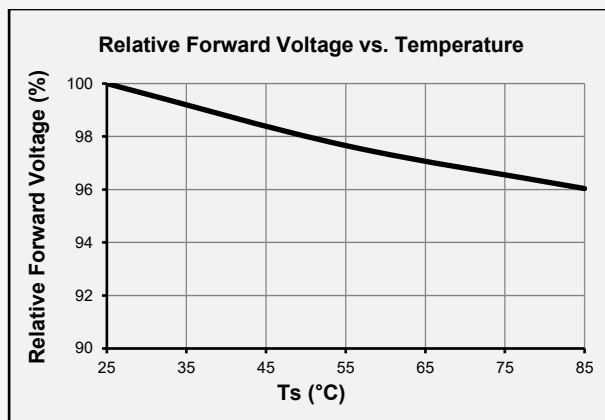
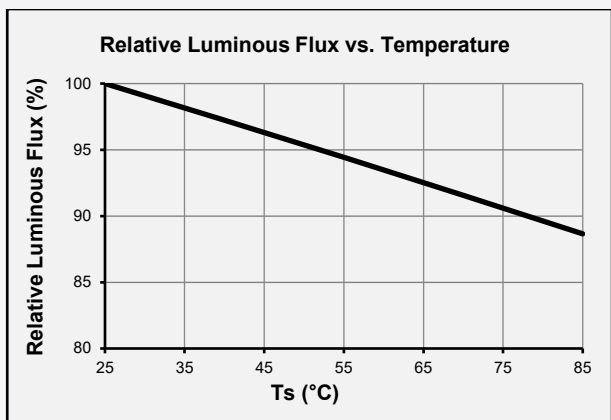
a) Spectrum Distribution ($I_F = 100 \text{ mA}$, $T_a = 25 \text{ }^\circ\text{C}$)



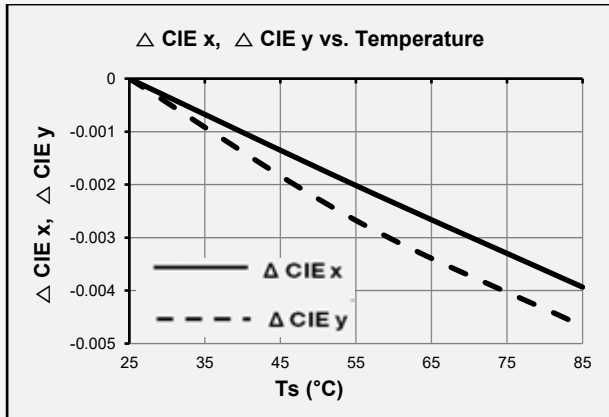
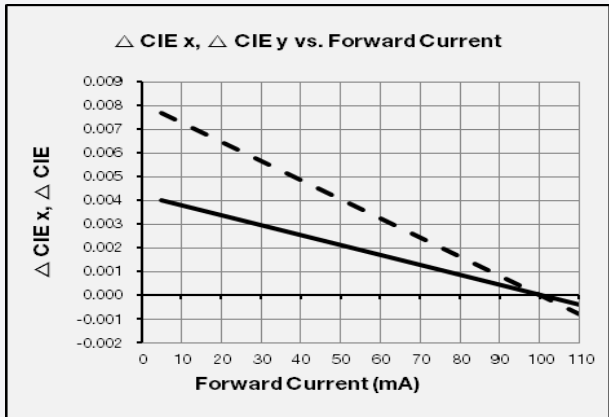
b) Forward Current Characteristics ($T_s = 25 \text{ }^\circ\text{C}$)



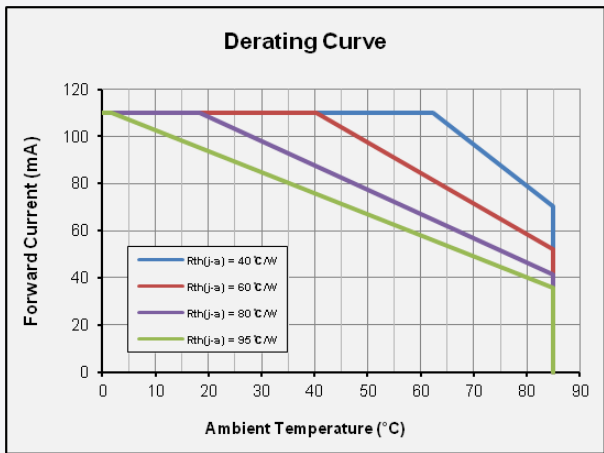
c) Temperature Characteristics ($I_F = 100 \text{ mA}$)



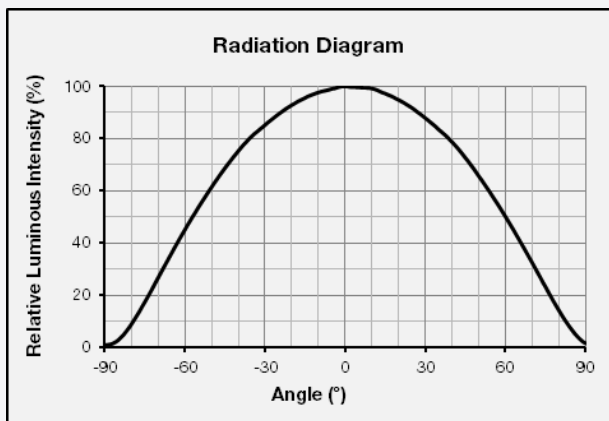
d) Color Shift Characteristics ($I_F = 100 \text{ mA}$, $T_s = 25 \text{ }^\circ\text{C}$)



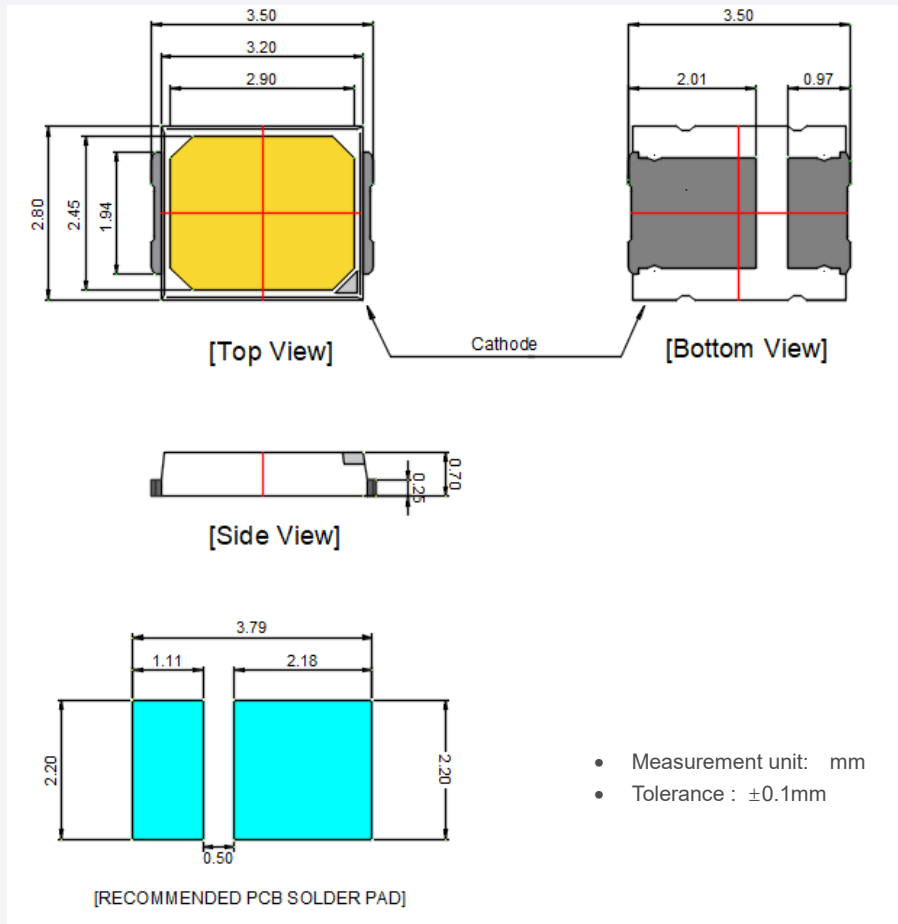
e) Derating Curve



f) Beam Angle Characteristics ($I_F = 100 \text{ mA}$, $T_s = 25 \text{ }^\circ\text{C}$)



4. Outline Drawing & Dimension



Notes:

1) T_s point and measurement method:

- ① Measure one point at the cathode pad, if necessary remove PSR of PCB to reach T_s point.
- ② All pads must be soldered to the PCB to dissipate heat properly, otherwise the LED can be damaged.

Precautions:

- 1) Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.
- 2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
- 3) Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

5. Reliability Test Items & Conditions

a) Test Items

Test Item	Test Condition	Test Hour / Cycle	Sample No.
Room Temperature Life Test	25 °C, DC 110 mA	1000 h	22
High Temperature Life Test	85 °C, DC 110 mA	1000 h	22
High Temperature Humidity Life Test	60 °C, 90 % RH, DC 110 mA	1000 h	22
Low Temperature Life Test	-40 °C, DC 110 mA	1000 h	22
Powered Temperature Cycle Test	-45 °C ~ 85 °C, each 20 min, on/off 5 min Temp. Change time 100min, DC 110 mA	100 cycles	22
Temperature Cycling	-45 °C / 15 min ↔ 125 °C / 15 min	200 cycles	100
High Temperature Storage	85 °C	1000 h	11
Low Temperature Storage	-40 °C	1000 h	11
ESD (HBM)	<p> R₁: 10 MΩ R₂: 1.5 kΩ C: 100 pF V: ±2 kV </p>	5 times	30

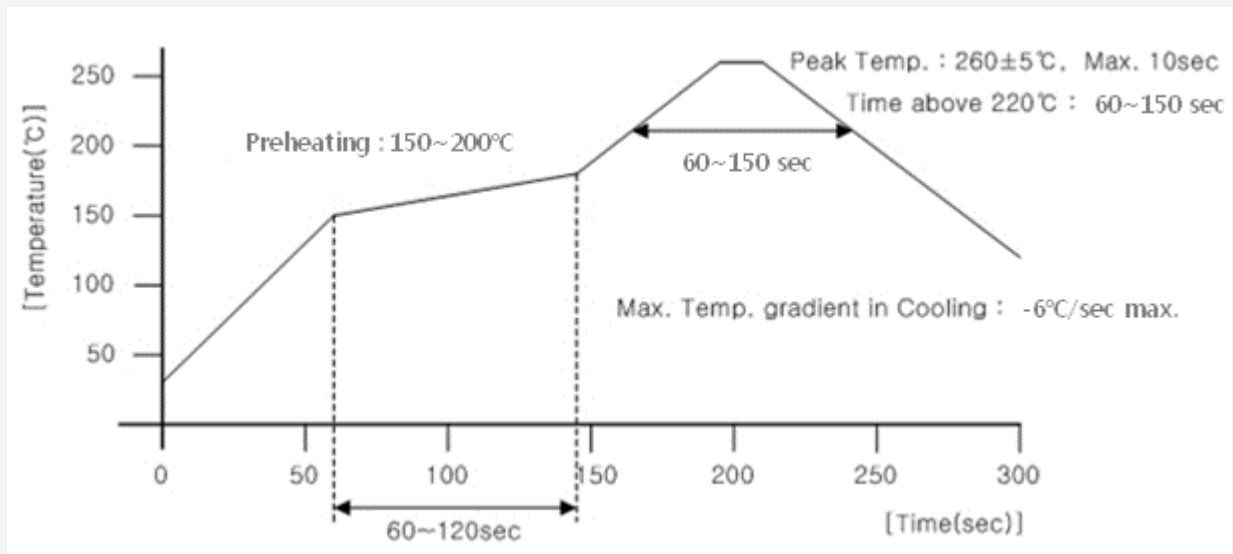
b) Criteria for Judging the Damage

Item	Symbol	Test Condition (T _s = 25 °C)	Limit	
			Min	Max
Forward Voltage	V _F	I _F = 100 mA	Init. Value * 0.9	Init. Value * 1.1
Luminous Flux	Φ _v	I _F = 100 mA	Init. Value * 0.7	Init. Value * 1.1

6. Soldering Conditions

a) Reflow Conditions (Pb free)

Reflow frequency: 2 times max.

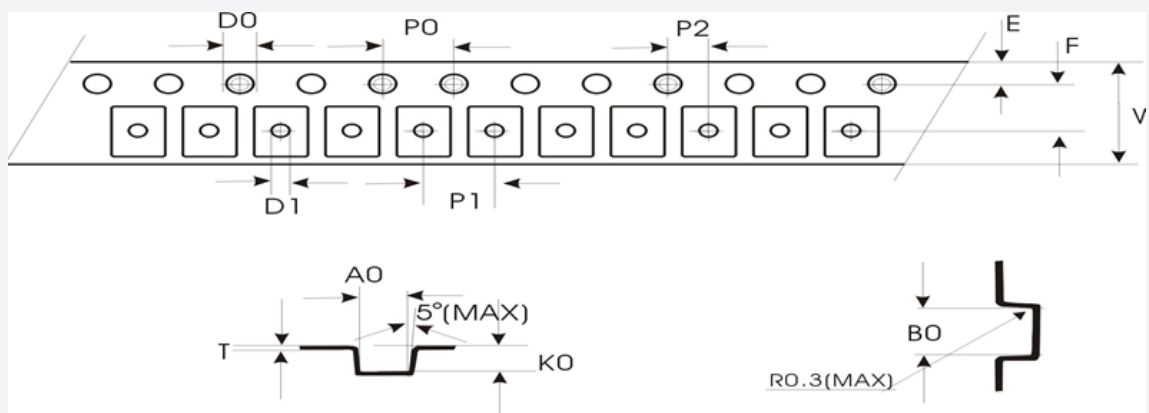


b) Manual Soldering Conditions

Not more than 5 seconds @ max. 300 °C, under soldering iron.

7. Tape & Reel

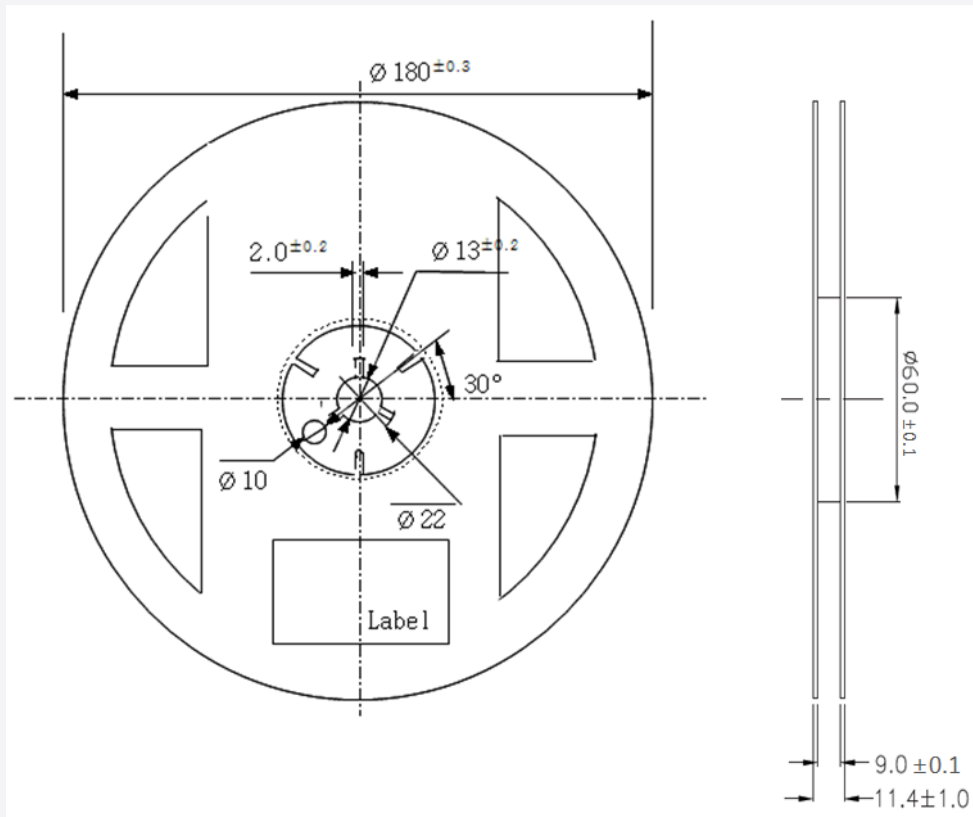
a) Taping Dimension



A0	3.10 ± 0.1	P0	4.00 ± 0.1	T	0.20 ± 0.05	D0	1.60(MAX)
B0	3.70 ± 0.1	P1	4.00 ± 0.1	E	1.75 ± 0.1	D1	1.05(MIN)
K0	1.00 ± 0.1	P2	2.00 ± 0.1	F	3.50 ± 0.05	V	8.00 ± 0.1

b) Reel Dimension (max 4,000 pcs)

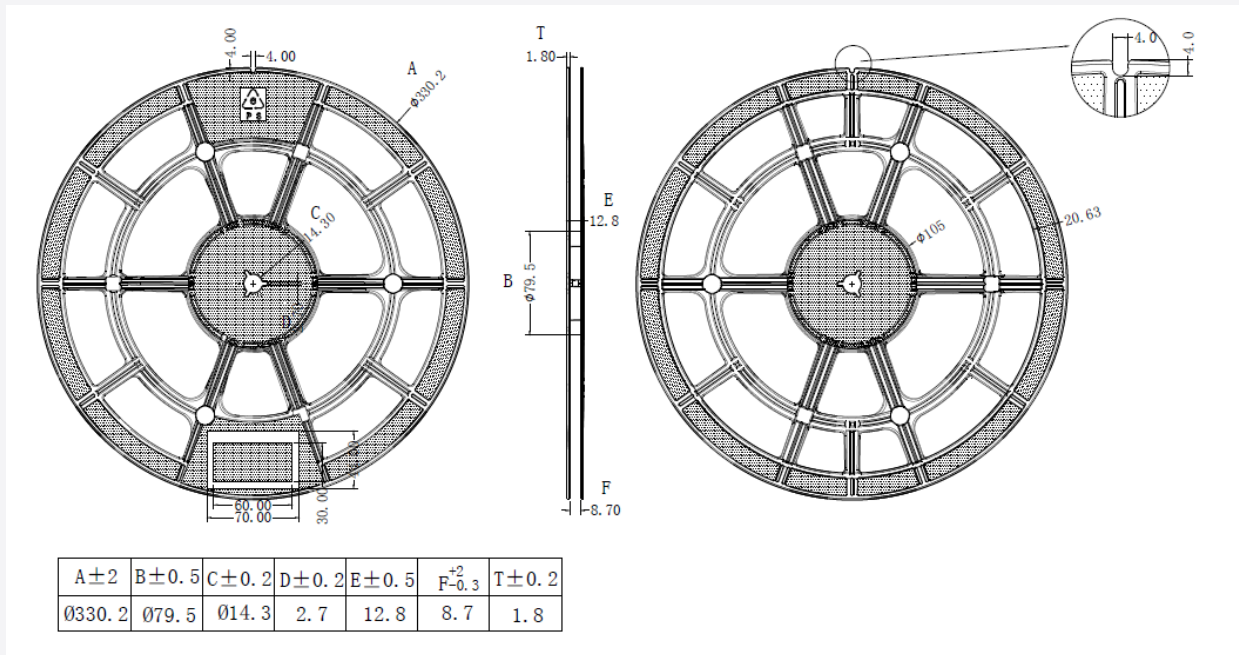
(unit: mm)

**Notes:**

- 1) Quantity: The quantity/reel is 4,000 pcs
- 2) All dimensions are millimeters (tolerance : $\pm 0.2\text{mm}$)
- 3) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

c) Reel Dimension (max 16,000 pcs)

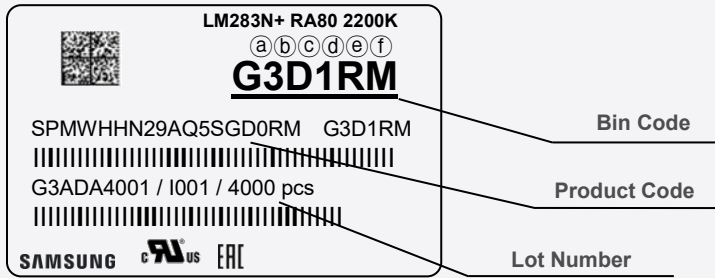
(unit: mm)

**Notes:**

- 1) Quantity: The quantity/reel is 16,000 pcs
- 2) All dimensions are millimeters (tolerance : ±0.2mm)
- 3) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

8. Label Structure

a) Label Structure



Note: Denoted bin code and product code above is only an example (see description on page 6)

Bin Code:

- ⒶⒷ: Forward Voltage bin (refer to page 10)
- ⒸⒹ: Chromaticity bin (refer to page 8-15)
- ⒺⒻ: Luminous Flux bin (refer to page 7)

b) Lot Number

The lot number is composed of the following characters:



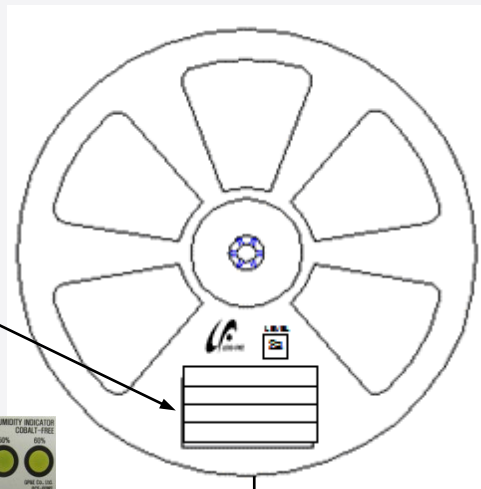
①②③④⑤⑥⑦⑧⑨ / |ⒶⒷⒸ / 4,000 pcs

- ①② : Production site (G3 or GP : Shenzhen, China, G4 : Guangzhou, China)
- ③ : Product state (A: Normal, B: Bulk, C: First Production, R: Reproduction, S: Sample)
- ④ : Year (C: 2018, D: 2019, E: 2020, F: 2021, G: 2022...)
- ⑤ : Month (1~9, A, B, C)
- ⑥ : Day (1~9, A, B~V)
- ⑦⑧⑨ ⒶⒷⒸ : Product serial number

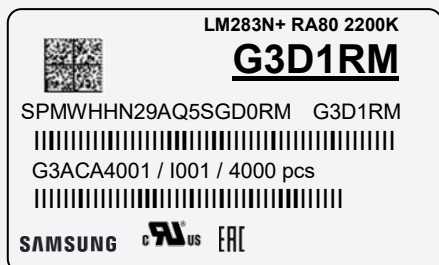
9. Packing Structure

a) Packing Process (The quantity of PKG on the Reel to be Max 4,000 pcs)

Reel



Aluminum Vinyl Packing Bag

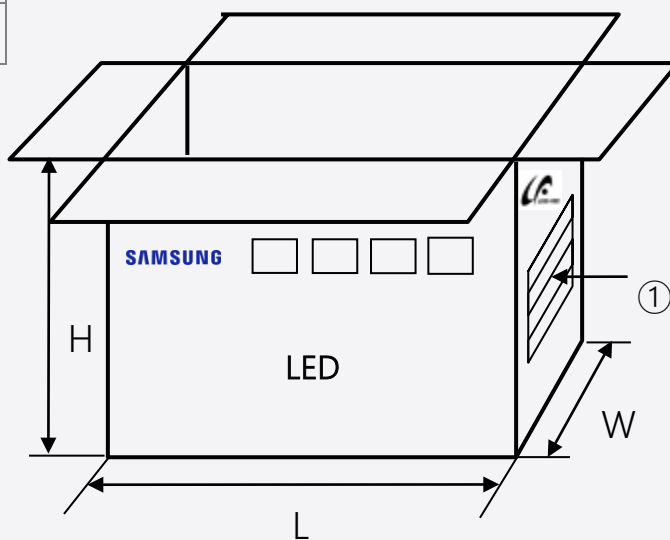
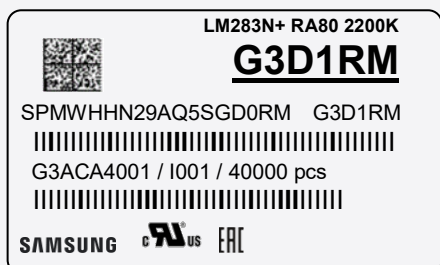


Outer Box

Material: Paper (SW3B(B))

Type	Size (mm)			Note
	L	W	H	
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels
7 inch S	245 ± 5	220 ± 5	86 ± 5	Up to 5 reels

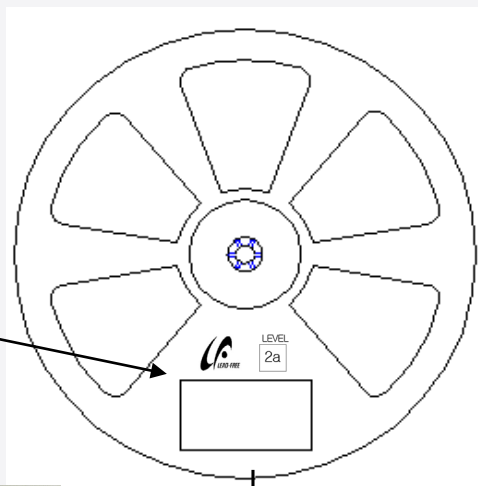
① Side Label



b) Packing Process (The quantity of PKG on the Reel to be Max 16,000 pcs)

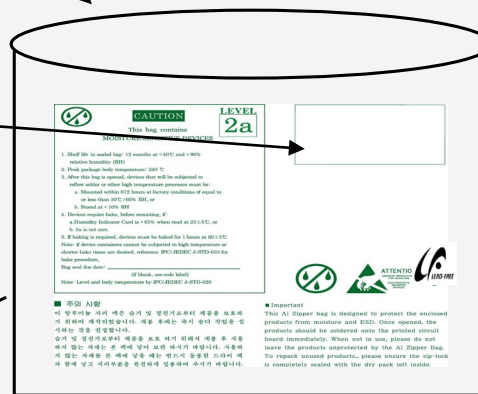
Reel

LM283N+ RA80 2200K
G3D1RM
 SPMWHHN29AQ5SKD0RM G3D1RM
 G3ACA4001 / IOAA / 16000 pcs
 SAMSUNG C^{RU} US EAC



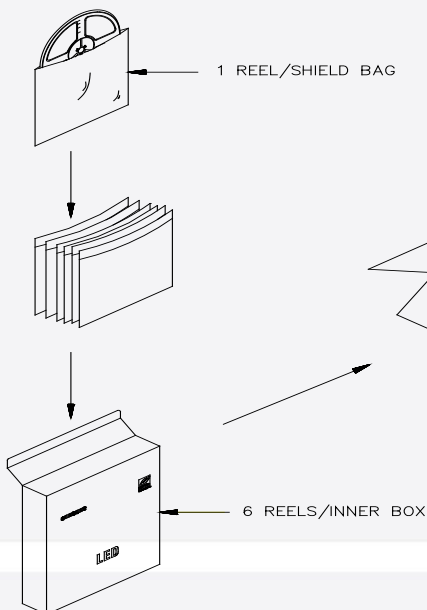
Aluminum Vinyl Packing Bag

LM283N+ RA80 2200K
G3D1RM
 SPMWHHN29AQ5SKD0RM G3D1RM
 G3ACA4001 / IOAA / 16000 pcs
 SAMSUNG C^{RU} US EAC



Outer Box

LM283N+ RA80 2200K
G3D1RM
 SPMWHHN29AQ5SKD0RM G3D1RM
 G3ACA4001 / IOAA / 480000 pcs
 SAMSUNG C^{RU} US EAC



5 INNER/OUTER BOX

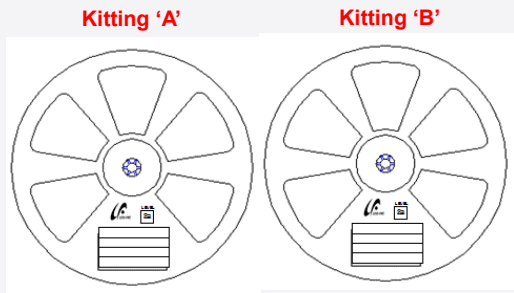
c) Packing Process (The quantity of PKG on the Reel to be Max 4,000 pcs)

Reel

Kitting 'A'

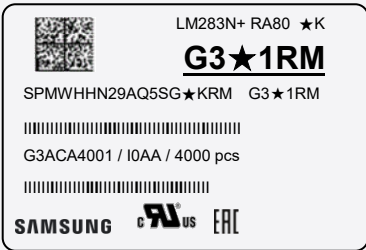


Kitting 'B'

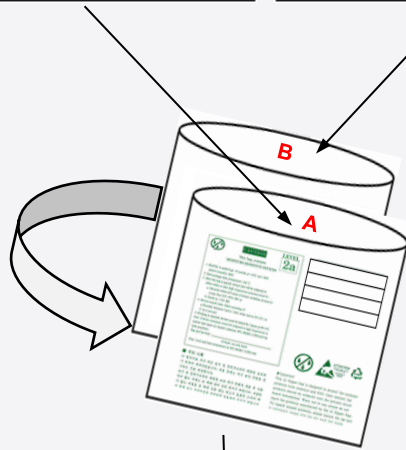
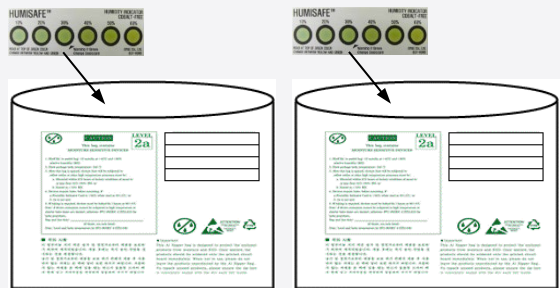


Aluminum Vinyl Packing Bag

Kitting 'A'

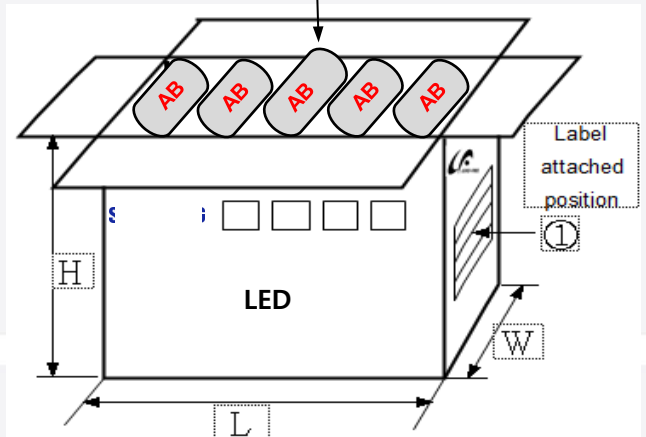


Kitting 'B'



Material: Paper (SW3B(B))

Type	Size (mm)			Note L
	L	W	H	
7 inch L	245 ± 5	220 ± 5	7 inch L	245 ± 5

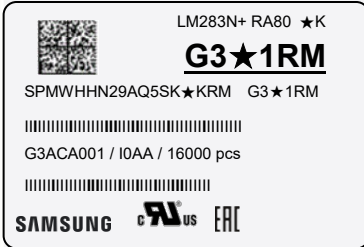


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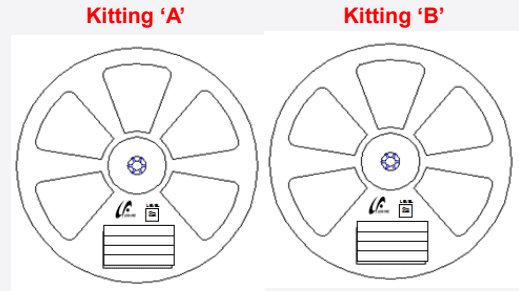
d) Packing Process (The quantity of PKG on the Reel to be Max 16,000 pcs)

Reel

Kitting 'A'



Kitting 'B'

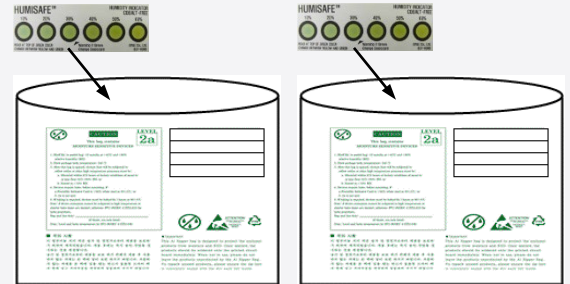


Aluminum Vinyl Packing Bag

Kitting 'A'

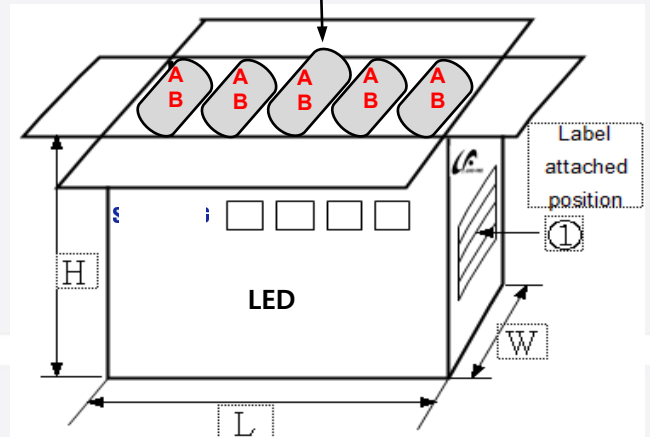


Kitting 'B'



Material: Paper (SW3B(B))

Type	Size (mm)			Note L
	L	W	H	
13 inch	378 ± 5	345 ± 5	13 inch	378 ± 5



e) Aluminum Vinyl Packing Bag



CAUTION

This bag contains
MOISTURE SENSITIVE DEVICES

LEVEL

2a

LM283N+ RA80 2200K

G3D1RM

SPMWHHN29AQ5SGD0RM G3D1RM
 G3ACA4001 / I001 / 4,000 pcs

SAMSUNG 






주의 사항

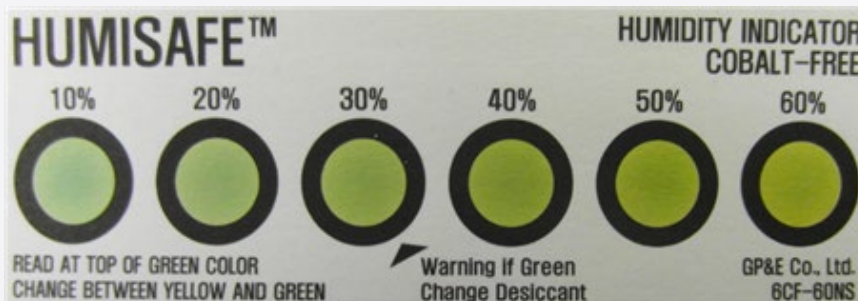
이 알루미늄 지퍼 백은 습기 및 정전기로부터 제품을 보호하기 위하여 제작되었습니다. 개봉 후에는 즉시 솔더 작업을 실시하는 것을 권장합니다.

습기 및 정전기로부터 제품을 보호하기 위해서 개봉 후 사용하지 않는 자재는 본 팩에 넣어 보관하시기 바랍니다. 사용하지 않는 자재를 본 팩에 넣을 때는 반드시 동봉된 드라이 팩과 함께 넣고 지퍼부분을 완전하게 밀봉하여 주시기 바랍니다.

Important

This Al Zipper bag is designed to protect the enclosed products from moisture and ESD. Once opened, the products should be soldered onto the printed circuit board immediately. When not in use, please do not leave the products unprotected by the Al Zipper Bag. To repack unused products., please ensure the zip-lock is completely sealed with the dry pack left inside.

f) Humidity Indicator Card inside Aluminum Vinyl Bag



10. Precautions in Handling & Use

- 1) For over-current protection, users are recommended to apply resistors connected in series with the LEDs to mitigate sudden change of the forward current caused by shift of forward voltage.
- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When cleaning is required, IPA is recommended as the cleaning agent. Some solvent-based cleaning agent may damage the silicone resins used in the device.
- 3) When the device is in operation, the forward current should be carefully determined considering the maximum ambient temperature and corresponding junction temperature.
- 4) LEDs must be stored in a clean environment.
(shelf life of sealed bags is 12 months at temperature 0~40 °C, 0~90 % RH).
- 5) After storage bag is opened, device subjected to soldering, solder reflow, or other high temperature processes must be:
 - a. Mounted within 672 hours (28 days) at an assembly line with a condition of no more than 30 °C / 60 % RH*^{Note 1}, or
 - b. Mounted within 24 hours (1 day) at an assembly line with a condition of more than 30 °C / 70 % RH*^{Note 2}, or
 - c. Stored at <10 % RH.

*Note 1, 2: IPC/JEDEC J-STD-033A, Recommended Equivalent Total Floor Life Table

Package Type and Body Thickness	Moisture Sensitivity Level	Maximum Percent Relative Humidity						Temperature
		40%	50%	60%	70%	80%	90%	
Body Thickness <2.1mm	Level 2a	∞	∞	28	1	1	1	30°C
		∞	∞	∞	2	1	1	25°C
		∞	∞	∞	2	2	1	20°C

- 6) Repack unused devices with anti-moisture packing, fold to close any opening and then store in a dry place.
- 7) Devices require baking before mounting, if humidity card reading is >60 % at 23 ± 5 °C.
- 8) Devices must be baked for 10~24 hours at 60 ± 5 °C, if baking is required.
- 9) The LEDs are sensitive to the static electricity and surge current. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs. If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices. Damaged LEDs may show some unusual characteristics such as increase in leakage current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.
- 10) VOCs (Volatile Organic Compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead to a discoloration of encapsulant when they exposed to heat or light. This phenomenon can cause a significant loss of light emitted (output) from the luminaires. In order to prevent these problems, we recommend users to know the physical properties of materials used in luminaires and they must be carefully selected.
- 11) Risk of sulfurization (or tarnishing)
The LED from Samsung uses a silver-plated lead frame and its surface color may change to black (or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound. Sulfurization of lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases, open circuit. It requires caution. Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials such as rubber, plain paper, lead solder cream, etc.

Legal and additional information.

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Samsung provides limited warranty for its LED products, the full text of which is available at <https://www.samsung.com/led/support/warranties>

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