# VEMD1060X01

## **Vishay Semiconductors**



# **Silicon PIN Photodiode**



### DESCRIPTION

VEMD1060X01 is a high speed and high sensitive PIN photodiode with a highly linear photoresponse. It is a low profile surface mount device (SMD) including the chip with a 0.23 mm<sup>2</sup> sensitive area detecting visible and near infrared radiation.

### FEATURES

- Package type: surface mount
- Package form: 0805 top view
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- Radiant sensitive area (in mm<sup>2</sup>): 0.23
- AEC-Q101 qualified
- High photo sensitivity
- High radiant sensitivity
- Excellent I<sub>ra</sub> linearity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity:  $\phi = \pm 70^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- High speed photo detector
- Small signal detection
- Proximity sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ (deg)	λ <sub>0.1</sub> (nm)	
VEMD1060X01	1.8	± 70	350 to 1070	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMD1060X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805 top view		

#### Note

• MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	20	V	
Power dissipation	T <sub>amb</sub> ≤ 25 °C	Pv	215	mW	
Junction temperature		Тj	110	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +110	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C	
Soldering temperature	According to reflow solder profile Fig. 6	T <sub>sd</sub>	260	°C	
Thermal resistance junction / ambient	According to EIA / JESD 51	R <sub>thJA</sub>	270	K/W	

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ROHS COMPLIANT

HALOGEN

FREE GREEN

(5-2008)

# VEMD1060X01



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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	0.9	1.1	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	20	-	-	V
Reverse dark current	$V_{R} = 10 V, E = 0$	I <sub>ro</sub>	-	0.01	5	nA
Diode capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD	-	3.8	-	pF
	V <sub>R</sub> = 3 V, f = 1 MHz, E = 0	CD	-	1.7	-	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo	-	350	-	mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>	-	-2.6	-	mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>	-	1.8	-	μA
Temperature coefficient of ${\rm I}_{\rm k}$	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 835 \text{ nm}$	TK <sub>lk</sub>	-	0.1	-	%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, V_R = 5 \text{ V}$	I <sub>ra</sub>	1.4	1.8	3	μA
	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 890 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	-	2.6	-	μA
Angle of half sensitivity		φ	-	± 70	-	deg
Wavelength of peak sensitivity		λρ	-	820	-	nm
Range of spectral bandwidth		λ <sub>0.1</sub>	-	350 to 1070	-	nm
Rise time	$V_{\text{R}}$ = 5 V, $R_{\text{L}}$ = 50 $\Omega$ , $\lambda$ = 830 nm	t <sub>r</sub>	-	60	-	ns
Fall time	$V_R$ = 5 V, $R_L$ = 50 $\Omega$ , $\lambda$ = 830 nm	t <sub>f</sub>	-	80	-	ns

### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

Basic characteristics graphs to be extended to 110 °C ambient temperatures where applicable.

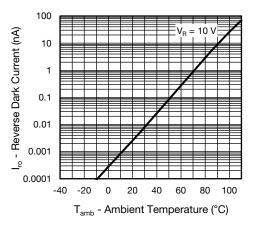


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

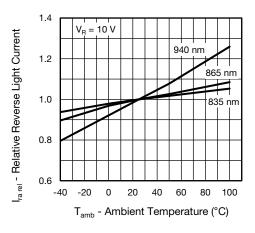


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



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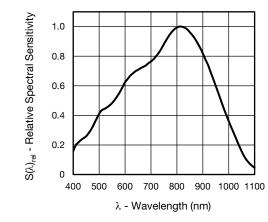


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

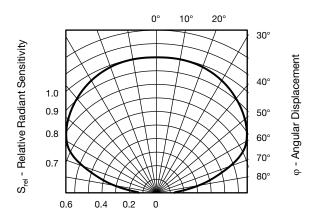
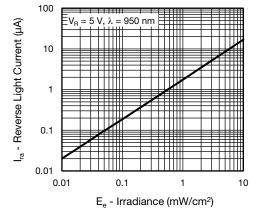


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement



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Fig. 3 - Reverse Light Current vs. Irradiance

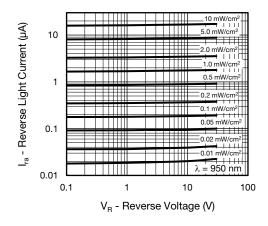


Fig. 4 - Reverse Light Current vs. Reverse Voltage

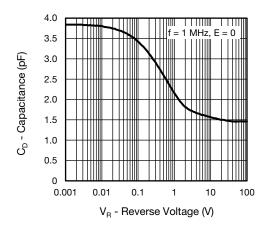


Fig. 5 - Diode Capacitance vs. Reverse Voltage

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## **Vishay Semiconductors**

## **REFLOW SOLDER PROFILE**

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ISHA

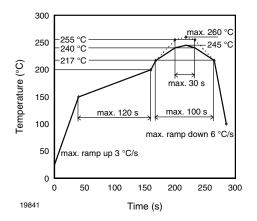


Fig. 8 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### **PACKAGE DIMENSIONS** in millimeters

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

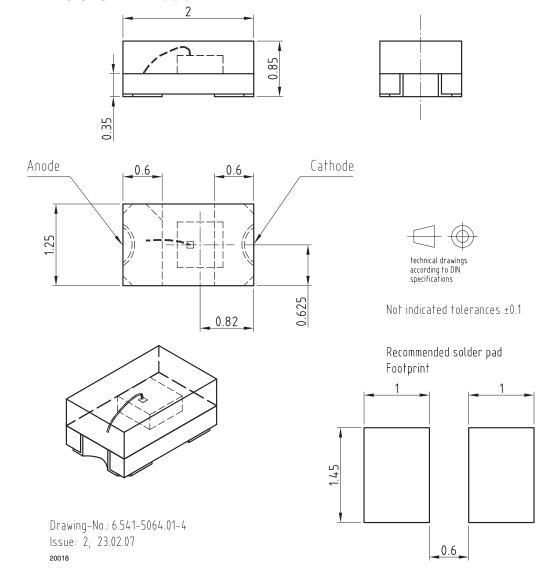
Floor life: 72 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 4, according to J-STD-020.

### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



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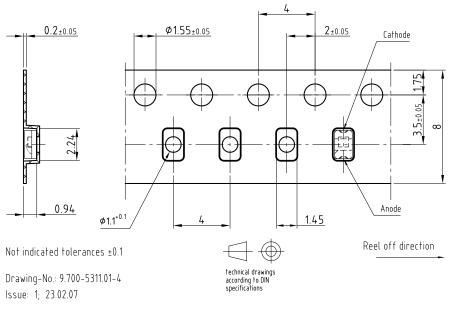
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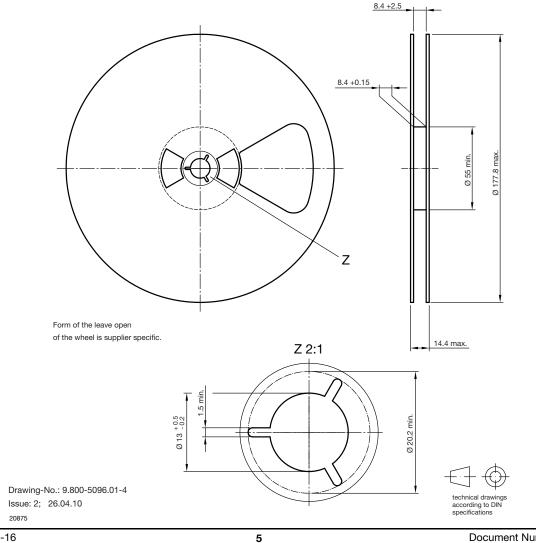
# **Vishay Semiconductors**



#### **BLISTER TAPE DIMENSIONS** in millimeters



### **REEL DIMENSIONS** in millimeters



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